

(19) World Intellectual Property
Organization
International Bureau



02 MAR 2005

(43) International Publication Date
25 March 2004 (25.03.2004)

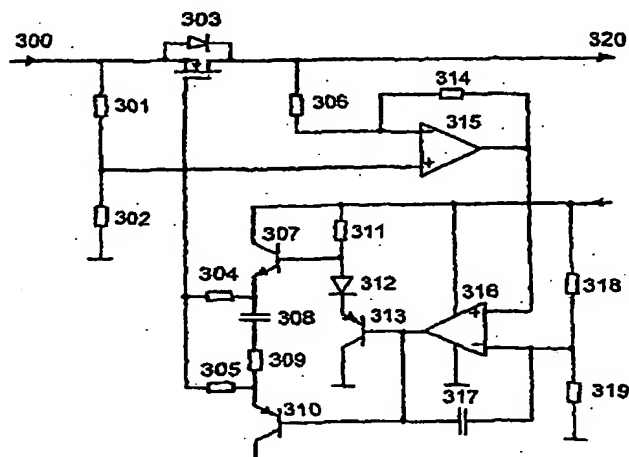
PCT

(10) International Publication Number
WO 2004/025801 A1

- (51) International Patent Classification: H02J 1/10 (74) Agent: ZACCO DENMARK A/S; Hans Bekkevolds Allé 7, DK-2900 Hellerup (DK).
- (21) International Application Number: PCT/DK2003/000595 (81) Designated States (national): AB, AG, AL, AM, AT (utility model), AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ (utility model), CZ, DE (utility model), DE, DK (utility model), DK, DM, DZ, EC, EE (utility model), EE, EG, ES, FI (utility model), FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT (utility model), PT, RO, RU, SC, SD, SE, SG, SK (utility model), SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (22) International Filing Date: 15 September 2003 (15.09.2003)
- (25) Filing Language: Danish
- (26) Publication Language: English
- (30) Priority Data: PA 2002 01361 16 September 2002 (16.09.2002) DK
- (71) Applicant (for all designated States except US): SCAN-POWER [DK/DK]; Georginevej 2, DK-2970 Hørsholm (DK).
- (72) Inventor; and
- (75) Inventor/Applicant (for US only): SEIERSEN, Ole, Steen [DK/DK]; Georginevej 2, DK-2970 Hørsholm (DK).
- (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

[Continued on next page]

(54) Title: ORING CIRCUIT



(57) Abstract: A method and a circuit comprising an electronically controlled transistor element (303) configured for conveying a current from an input terminal (300) to an output terminal (320), wherein the current is of a magnitude belonging within a pre-determined current range; and a control circuit configured for controlling said transistor element in such a manner that the transistor element (303) is controlled to produce a pre-selected voltage drop across the transistor element independently of the traversing current. Hereby an extremely effective circuit is provided that has high efficiency for transporting large current values from a supply module to a power consuming module and hence the option is provided of substituting the circuit according to the invention in an active system; a so-called hot swap. The circuit can also easily be dimensioned to a desired application by adding a number of transistor elements in parallel.

WO 2004/025801 A1